

Sheet 1 of 1

INFORMATION DISCLOSURE
CITATION

ATTY. DOCKET NO.

SERIAL NO.

2380-152

09/712,123

APPLICANT

HORLIN et al

FILING DATE

November 15, 2000

GROUP

unknown

U.S. PATENT DOCUMENTS

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JUL 14 2001

Technology Center 2600

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TRANSLATION

00/38380

6-2000

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YES

NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

h Mirfakhraei, "Wafer-Scale Integration as a Technology Choice for High Speed ATM Switching Systems", Proceedings of the Annual International Conference on Wafer Scale Integration (from 1996 Proceedings of the Int'l. Conference on Innovative Systems in Silicon), US, New York, IEEE, vol. CONF. 6, 1994, pp. 282-291

r Doi et al, "A High-Speed ATM Switch Architecture for FTTH - An ATM Switch Architecture with Input and Cross-Point Buffers", Proceedings of the International Switching Symposium, DE, Berlin, VDE Verlag, vol. SYMP. 15, 23 April 1995, pp. 384-388

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Form PTO-FB-A820 (Also PTO-144)

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(Use several sheets if necessary)

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